

Notice of References Cited	Application/Control No. 09/935,692		Applicant(s)/Patent Under Reexamination SUZUKI ET AL.	
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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-3,703,725	11-1972	Gomersall et al.	700/97
*	B	US-5,548,518	08-1996	Dietrich et al.	700/100
*	C	US-6,198,980	03-2001	Costanza, John R.	700/99
*	D	US-6,631,305	10-2003	Newmark, Larry J.	700/110
*	E	US-2003/0109950	06-2003	Andrade et al.	700/103
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Deckro, Richard F., Balancing Cycle Time and Workstations IIE Transactions, June 1989, Vol. 21, No. 2, Pages 106-111
	V	Faaland, Bruce H. et al., Assembly Line Balancing with Resource Dependent Task Times Decision Sciences, March/April 1992, Vol. 23, No. 2, Pages 343-364
	W	Raviv, Amnon, Applications of Queuing Theory and Simulation to Staffing in the Semiconductor Clean Room Environment 1995 International Symposium on Semiconductor Manufacturing, Pages 252-256
	X	Ferrell, David S., Manufacturing Modeling and Optimization 1995 IEEE/SEMI Advanced Semiconductor Manufacturing Conference, pages 334-339

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

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	A	US-			
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

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	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Zavadkav, Emil et al., Self-buffering, Self-balancing, Self-flushing Production Lines Management Science, August 1996, Vol. 42, No. 8, Pages 1151-1164
	V	Kiritsis D. et al., Petri net techniques for process planning cost estimation Advances in Software Engineering, 1999, Pages 375-387
	W	Gel Esma Senturk, Stochastic models of workforce agility in production systems Northwestern University, Dissertation, 1999, AAT 9953370, Abstract
	X	Just-In-Time for Operators (Shopfloor Series) Productivity Press, June 1998, ISBN: 1563271338

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.